

Power Semiconductor Mounting Board Thermal Character Evaluation/ Analysis Equipment

TE100





Power Semiconductor Mounting Substrate Thermal Characteristics Evaluation Equipment

International Workshop for Advanced System Integration and Packaging (ASIP) Purdue University

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Yamato Scientific Co.,Ltd.



Background

With the accelerated development of next-generation power semiconductors (SiC and GaN), power electronics devices are becoming smaller and more powerful, and the market for power electronics devices is expected to expand, including electric vehicles.

However, power electronics devices are exposed to high temperature conditions, resulting in efficiency degradation and reliability issues. In order to solve these issues, manufacturers of components and power electronics equipment are aiming for higher heat dissipation and higher reliability.

Since Nov. 2016, we have been conducting joint research with Dr. Suganuma on "Evaluation Methods for Thermal Characteristics of Next Generation Power Electronics Substrates" Furthermore, a project to standardize the evaluation method to ISO standards is being conducted in parallel under the leadership of Dr. Suganuma.

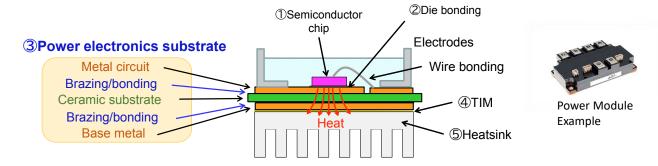


Challenge: Thermal performance evaluation

General Power Device Structure

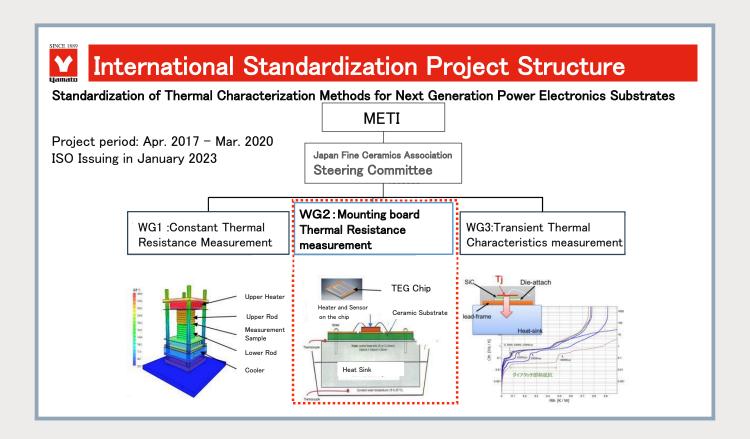
Main path of generated heat (250° C)

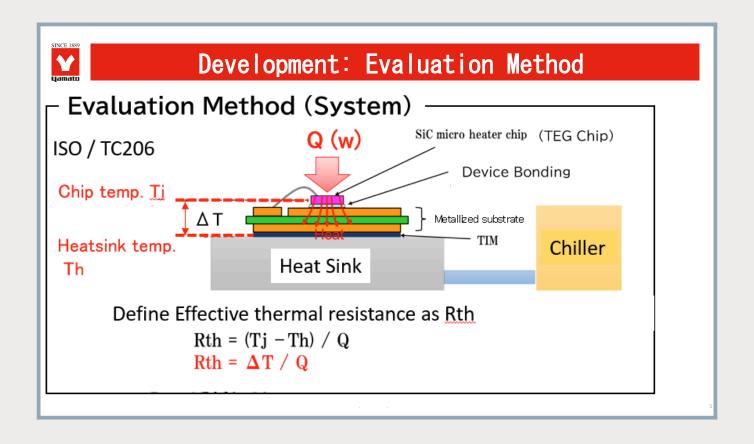
①Semiconductor chip \Rightarrow ②Die Bonding \Rightarrow ③ Power electronics substrate \Rightarrow ④TIM \Rightarrow ⑤ Heatsink

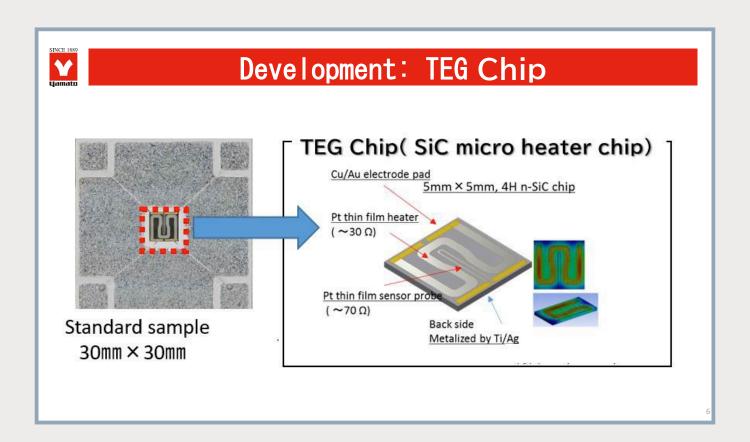


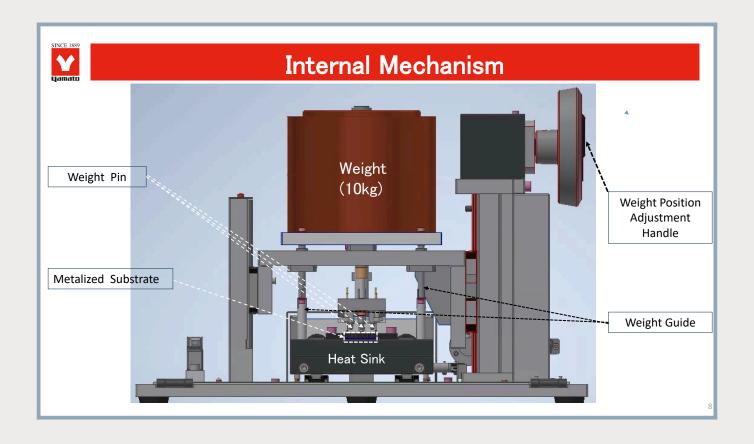
Optimization of high thermal conductivity and high heat dissipation design for each component is required

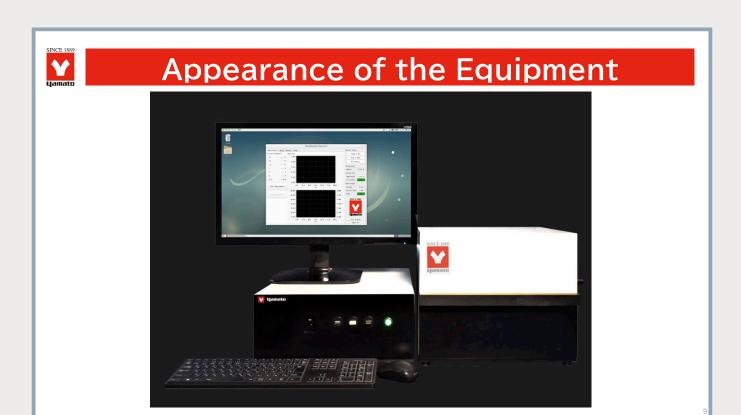
⇒ More accurate thermal characterization is needed.

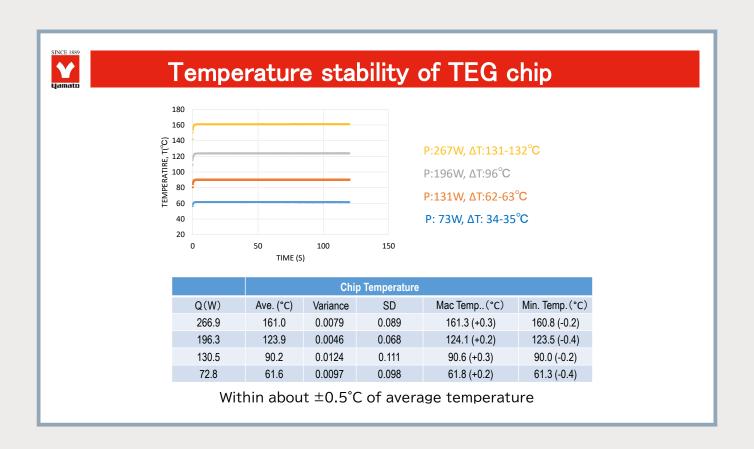


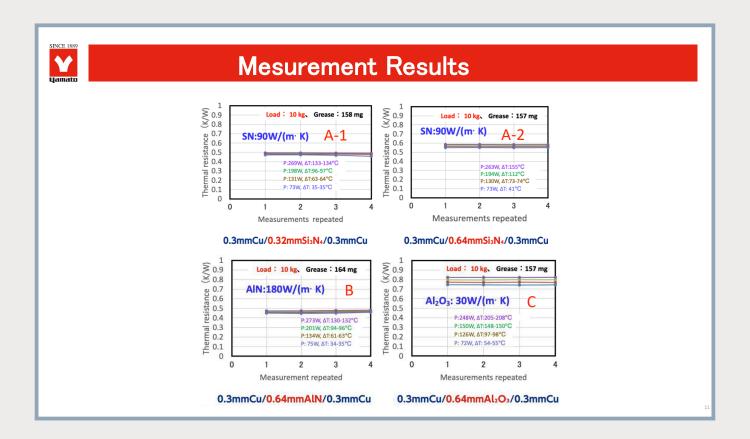














Proposal to ISO

O Progress

Jan. 2020: NP proposal to ISO

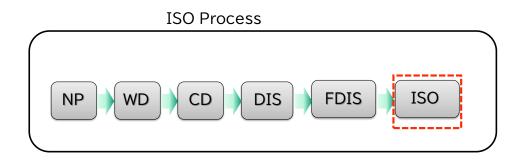
Sep. 2022: FDIS approval

Jan. 2023: ISO Issuing

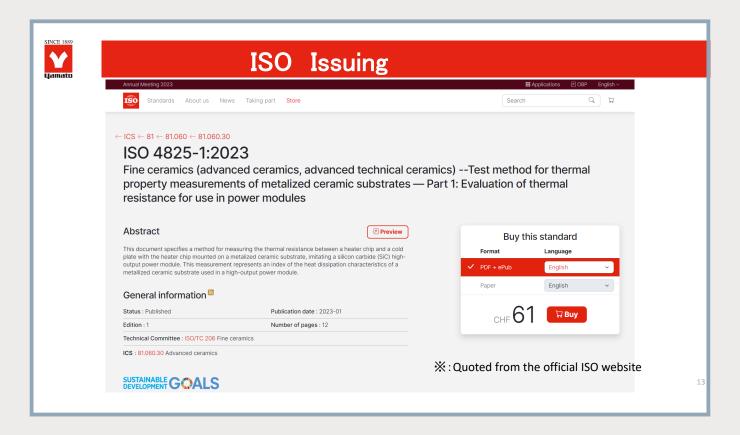
NP: New Proposal

DIS: Draft of International Standard

FDIS: Final DIS



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Conclusion

Thermal characteristics evaluation and analysis equipment + SiC TEG chip

- 1. Effective thermal resistance of power electronics boards can be easily evaluated
- 2. TEG chip can simulate power module drive conditions.
- 3. TEG chip 's platinum probe enables accurate and stable temperature measurement.
- 4. Windows-like software is installed for easy operation.
- 5. Evaluation method is standardized in ISO4825-1:2023

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The specifications and performance figures of the products listed in this catalog are presented as a user guide under general operating conditions. When using the product, please understand the contents of the instruction manual and use the product correctly.

Please note that we cannot be held responsible for any damage to persons or property caused by using the product outside the conditions of use described in the instruction manual.

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